Product Summary

The LF357J is the first monolithic JFET input operational amplifier to incorporate well matched, high voltage JFETs on the same chip wirh standard bipolar transistor. The feature low input (bias and offset currents/low offset voltage and offset voltage drift, coupled with offset ajust which does not degrade drift or common mode rejection. The LF357J is also designed for high slew rate, wide bandwidth, extremely fast setting time, low voltage and current nosie and a low 1/f noise corner.

Parametrics

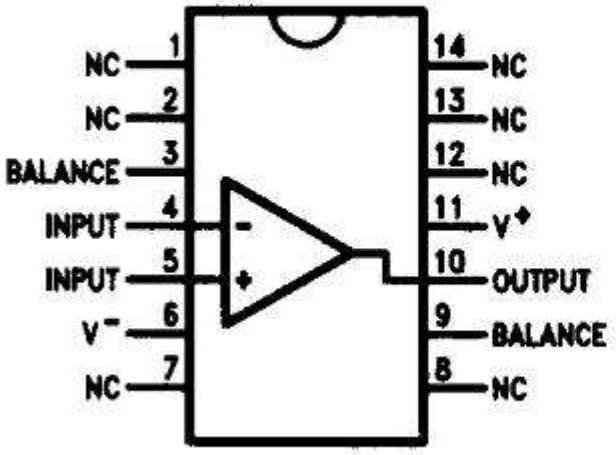
LF357J absolute maximum ratings: (1) Supply Voltage: ±22V; (2) Differential Input Voltage: ±40V; (3) Input Voltage Range: ±20V; (4) Output Short Circuit Duration: COntinuous; (5) Storage Temperature Range : -65 to +150°C; (6) ESD tolerance (100pF discharged through 1.5Ω) : 1200V.

Features

LF357J features: (1) Low Input bias current:30pA; (2) Low Input Offset current: 3pA; (3) high input impedance; (4) Low input offset voltage: 1mV; (5) Low input offset voltage temp. drift: 3μV/°C; (6) Low input nosie current: 0.01pA/˜Hz; (7) high common-mode rejection ratio: 100dB; (8) Large dc voltage gain: 106dB.

Diagrams

Dual-In-Line Package (J)



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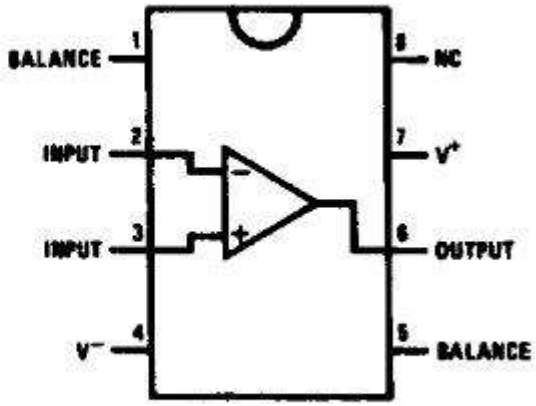
Dual-In-Line Package (M and N)



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Order Number